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APPLICATION NO.	FILING DATE	. FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/566,549	09/21/2006	Noriyoshi Matsuura	JP 030012	7331
7590 10/10/2007 Corporate Patent Counsel Philips Electronics North America Corporation			EXAMINER	
			MOONEY, MICHAEL P	
P O Box 3001 Briarcliff Manor, NY 10510		ART UNIT	PAPER NUMBER	
			2883	
			MAIL DATE	DELIVERY MODE
			10/10/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)				
	10/566,549	MATSUURA, NORIYOSHI				
Office Action Summary	Examiner	Art Unit				
·	Michael P. Mooney	2883				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on						
,	action is non-final.					
<i>,</i> —	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) Claim(s) 1-6 is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-3</u> is/are rejected.						
7)⊠ Claim(s) <u>4-7</u> is/are objected to.						
8) Claim(s) are subject to restriction and/o	r election requirement.					
Application Papers						
· · · · · <u>- ·</u>						
9) The specification is objected to by the Examiner.						
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)⊠ All b)□ Some * c)□ None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)).						
* See the attached detailed Office action for a list of the certified copies not received.						
Attachment(s)						
1) Notice of References Cited (PTO-892)	4)					
Notice of Draftsperson's Patent Drawing Review (PTO-948)     Information Disclosure Statement(s) (PTO/SB/08)     Paper No(s)/Mail Date	5) Notice of Informal F					

## **DETAILED ACTION**

Claims 5-7 are objected to under 37 CFR 1.75(c) as being in improper form because a multiple dependent claim cannot depend from any other multiple dependent claim. See MPEP § 608.01(n). Accordingly, the claims 5-7 have not been further treated on the merits.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-3 are rejected under 35 U.S.C. 102b as being anticipated by Kanaya et al. (6169593).

Kanaya et al. teaches (e.g., fig. 12B) an electronic apparatus comprising a conductive layer 28 supported by a substrate 20 and a metal layer 22 of a material having a resistivity lower than that of the conductive layer, the metal layer being extended on the conductive layer, the conductive layer 28 having an oxidation resistivity higher than that of the metal layer and forming a terminal for connecting to peripheral circuitry, wherein: the metal layer 22 (e.g., see fig. 9) extends on an extending portion of the conductive layer outside the terminal of the conductive layer, and/or on the periphery of or in the vicinity of a coupling area for making the conductive layer to be exposed to the exterior within an area of the terminal of the conductive layer 28; and there is provided an electrically insulating layer 50 (e.g., fig. 12B) which covers at least

a part of the terminal of the conductive layer 28 and the whole of the metal layer 22 and which extends on the area other than the coupling area within the area of the terminal of the conductive layer (fig. 12B; See also: figs. 2H, 5, 15A; col. 9 lines 17-29; col. 16 line 34 to col. 17 line 53). Thus claim 1 is met.

Kanaya et al. teaches (e.g., fig. 12B) an electronic apparatus comprising a conductive layer 28 supported by a substrate 20 and a metal layer 22 of a material having a resistivity lower than that of the conductive layer, the metal layer being extended on the conductive layer, the conductive layer 28 having an oxidation resistivity higher than that of the metal layer and forming a terminal for connecting to peripheral circuitry, wherein: the metal layer 22 (e.g., see fig. 9) extends in the vicinity of or along exclusively an edge of a coupling area (e.g., see area 9 of 28 in fig. 12B) extending substantially in parallel to a lineup direction of the terminals and/or an edge of the coupling area extending substantially at a right angle with the lineup direction, the coupling area being for making the conductive layer to be exposed to the exterior in an area of the terminal of the conductive layer (e.g., fig. 12B); and there is provided an electrically insulating layer 50 which covers at least a part of the terminal of the conductive layer and at least a main portion of the metal layer and which extends on the area other than the coupling area in an area of the terminal of the conductive layer (fig. 12B; See also: figs. 2H, 5, 15A; col. 9 lines 17-29; col. 16 line 34 to col. 17 line 53). Thus claim 2 is met.

Regarding claim 3, Kanaya et al. figure 2H details the use of anisotropic conducting film 80 to create an interface with bump electrode 19 of driving circuit 21. Thus claim 3 is met.

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael P. Mooney whose telephone number is 571-272-2422. The examiner can normally be reached during weekdays, M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Frank G. Font can be reached on 571-272-2415. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Michael P. Mooney

Examiner Art Unit 2883

FGF/mpm 9/22/07 Frank G. Font

Frank St Fort

**Supervisory Patent Examiner** 

Art Unit 2883